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PTO/SB/21 (09-04)

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TRANSMITTAL FORM

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Total Number of Pages in This Submission

14

Application Number

10/728,492

Filing Date

December 05, 2003

First Named Inventor

Hung, Eric

Art Unit

2818

Examiner Name

N/A

Attorney Docket Number

03-1237

ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to TC
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
<input type="checkbox"/> Amendment/Reply	<input type="checkbox"/> Petition	<input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)
<input type="checkbox"/> After Final	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Proprietary Information
<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address	<input type="checkbox"/> Status Letter
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Terminal Disclaimer	<input checked="" type="checkbox"/> Other Enclosure(s) (please identify below):
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<input checked="" type="checkbox"/> Information Disclosure Statement	<input type="checkbox"/> CD, Number of CD(s) _____	2. Copy of Cited Reference
<input type="checkbox"/> Certified Copy of Priority Document(s)	<input type="checkbox"/> Landscape Table on CD	3. Return Postcard
<input type="checkbox"/> Reply to Missing Parts/Incomplete Application		
<input type="checkbox"/> Reply to Missing Parts under 37 CFR 1.52 or 1.53		

Remarks

- IDS Filing

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	LSI Logic Corporation		
Signature			
Printed name	Henry Groth		
Date	6/6/2005	Reg. No.	39,696

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:

Signature			
Typed or printed name	Mark Salvatore	Date	6-8-05

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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In re Application of :

Serial No. : 10/728,492

Filed : December 05, 2003

For : Low Power Memory Controller
with Leaded Double Data Rate
DRAM Package Arranged on a
Two Layer Printed Circuit Board




Group Art Unit : 2818

Examiner :

Atty Docket : 5201-27600 / 03-1237

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Mark Salvatore

June 8, 2005 

Date Signature

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
408-433-7475

Date: 6/6/2005

Respectfully submitted,

Henry G. W. H.

Henry Groth

Reg. No. 39,696



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PTO/SB/08A (10-96)
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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE



INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known

Application Number	10/728,492
Filing Date	December-05, 2003
First Named Inventor	Eric Hung
Group Art Unit	2818
Examiner Name	
Attorney Docket No.	5201-27600 / 03-1237

Sheet 1 of 1

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where	T
		"TN-46-05, General DDR SDRAM Functionality", Micron Technology, July 2001	

Examiner signature		Date considered	
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